

# IQS-8115

## Transport Blazer

LAB AND MANUFACTURING – TRANSPORT AND DATACOM



### Fully integrated test solution supporting SONET/SDH test functions

- DS0/E0 to OC-48/STM-16 testing in a single module
- Supports SONET, SDH, DS<sub>n</sub> and PDH
- SmartMode automatic signal structure discovery with real-time simultaneous monitoring of all discovered STS/AU and user-selected VT/TU channels
- Intuitive, feature-rich user interface with available automated test scripting and multi-user remote management capabilities

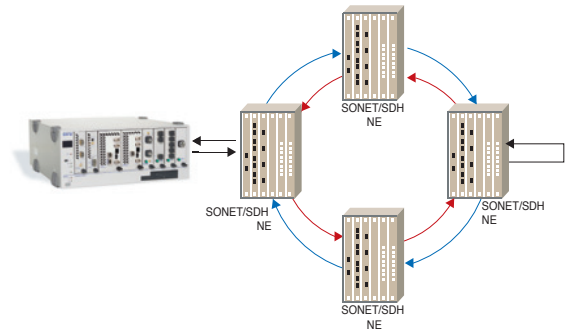
### Platform Compatibility

- IQS-600 Integrated Qualification System

# Advanced SONET/SDH Testing

EXFO's IQS-8115 Transport Blazer test module combines advanced DSn/PDH and SONET/SDH test functions in a single unit, eliminating the need for multiple, purpose-built test platforms for the testing of T1/E1 to OC-48/STM-16 circuits or network elements. The extensive list of DSn, SONET, PDH and SDH features available on the IQS-8115 Transport Blazer allows users to perform a wide range of tests from simple bit-error-rate (BER) analysis to more advanced characterization. These functions include:

- Mixed and bulk payload generation and analysis from 64 kbit/s to 2.5 Gbit/s
- High-order mappings: STS-1/3c/12c/48c and AU-3/AU-4/AU-4-4c/16c
- Low-order mappings: VT1.5/2/6, TU-11/12/2/3
- Section, line, high-order (HO) and low-order (LO) path overhead manipulation and monitoring
- Section, line, high-order and low-order path alarm/error generation and monitoring
- High-order and low-order pointer generation and monitoring
- Performance monitoring: G.821, G.826, G.828, G.829, M.2100, M.2101
- Frequency analysis and power measurement
- Frequency offset generation
- Automatic protection switching and service disruption time measurements
- Round-trip delay measurements
- DS1/DS3 auto detection of line code, framing and test pattern
- Dual DS1/DS3 receiver testing
- Independent transmitter and receiver testing
- Through mode analysis
- Programmable error/alarm injection
- DS1 FDL
- DS1 loopcodes and NI/CSU loopback emulation
- Fractional T1/E1 testing
- DS3 FEAC
- Tandem connection monitoring

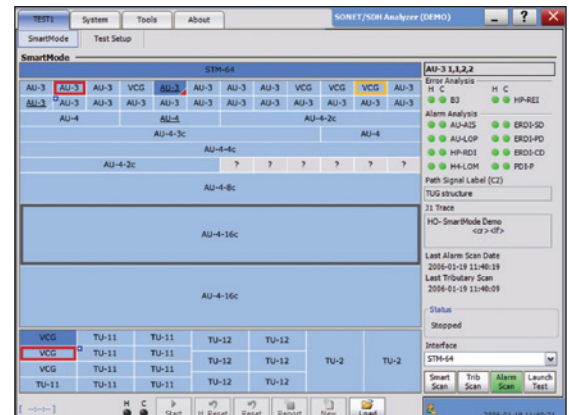


Housed in the IQS-600 platform, the IQS-8115 module enables advanced SONET/SDH characterization of systems/components.

## SmartMode: Real-Time Signal Structure Discovery and Monitoring

EXFO's IQS-8115 Transport Blazer supports a unique feature called SmartMode. This provides users with full visibility of all high-order (STS/AU) and low-order (VT/TU) mixed mappings within the incoming SONET/SDH test signal.

SmartMode automatically discovers the signal structure of the OC-n/STM-n line, including mixed mappings. In addition to this in-depth multichannel visibility, SmartMode performs real-time monitoring of all discovered high-order paths and user selected low-order paths simultaneously, providing users with the industry's most powerful SONET/SDH multichannel monitoring and troubleshooting solution. Real-time monitoring allows users to easily isolate network faults, saving valuable time and minimizing service disruption. SmartMode also provides one-touch test case start, allowing users to quickly configure a desired test path.



IQS-8115 SmartMode: multichannel signal discovery with real-time alarm scan (shown in the IQS-600 user interface).

## Unsurpassed Configuration and Operational Flexibility

The IQS-8115 Transport Blazer module is supported on EXFO's lab/manufacturing test platform, the IQS-600 Integrated Qualification System, which provides users with an all-in-one solution supporting a mix of Transport Blazer modules, Packet Blazer modules (IQS-8510G 10 Gigabit Ethernet, IQS-8510B Ethernet) and optical-layer test modules, making it the industry's first truly integrated lab/manufacturing testing platform. The resulting modularity enables users to upgrade their test system according to their specific requirements.



The IQS-8115 module is supported on EXFO's powerful lab/manufacturing platform, the IQS-600 Integrated Qualification System.

### Remote Management

Through its optional Visual Guardian Lite™ management software, the IQS-8115 Transport Blazer module allows you to perform remote testing and data analysis, as well as remote monitoring via standard Ethernet.

### Automated Test Scripting

The IQS-8115 Transport Blazer supports two methods for automated test scripting. First, by using industry-based SCPI commands, and second, by using a built-in macro recorder that facilitates the recording of test actions and allows to automatically create test scripts.

### Test Logger and Reporting

EXFO's IQS-8115 Transport Blazer module supports a detailed test logger and test reporting tools, enabling users to view any errors/alarms that occurred during the test interval, which can then be used for post-processing of results.

ID	Date/Time	Data Path	Event	Duration	Count	Rate
1	00:00:00	TEST 1	StartEvent			
2	00:00:04	Bankam [1]DS1	AlarmAis	00:00:14		
3	00:00:18	Bankam [1]DS1	ErrorCrc6	00:00:01	1	1.63E-07
4	00:00:22	Bankam [1]DS1	AlarmAis	00:00:09		
5	00:00:31	Bankam [1]DS1	ErrorCrc6	00:00:01	13	1.14E-06
6	00:00:31	Bankam [1]DS1/Pattern	AlarmLss	00:00:01		
7	00:00:34	Bankam [1]	AlarmLos	00:00:04		
8	00:00:38	Bankam [1]	ErrorBpv	00:00:01	1	1.90E-08
9	00:00:40	Bankam [1]	ErrorBpv	00:00:01	1	3.60E-08
10	00:00:40	Bankam [1]DS1	ErrorCrc6	00:00:01	1	7.50E-07
11	00:00:40	Bankam [1]DS1/Pattern	ErrorBitError	00:00:01	1	5.43E-08
12	00:00:50	Bankam [1]DS1/Pattern	ErrorBitError	00:00:01	352935	1.04E-02
13	00:00:51	Bankam [1]DS1/Pattern	AlarmLss	00:00:04		

Test logger: a detailed, time-stamped list of all events occurring during test execution.

# Electrical Interfaces

The following section provides detailed information on all supported SONET/SDH electrical interfaces.

	DS1	E1/2M	E2/8M	E3/34M	DS3/45M	STS-1e/STM-0e/52M	E4/140M	STS-3e/STM-1e/155M			
Tx pulse amplitude	2.4 to 3.6 V	3.0 V	2.37 V	2.37 V	1.0 ± 0.1 V	0.36 to 0.85 V	1.0 ± 0.1 Vpp	0.5 V			
Tx pulse mask	GR-499 Figure 9.5	G.703 Figure 15	G.703 Figure 15	G.703 Figure 16	G.703 Figure 17	DS-3 GR-499 Figure 9-8	45-M G.703 Figure 14	GR-253 Figure 4-10/4-11	G.703 Figure 18/19	STS-3e GR-253 Figure 4-12,4-13,4-14	STM-1e/155M G.703 Figure 4-14/22, 23
Tx LBO preamplification	Power dBdsx +0.6 dBdsx (0-133 ft) +1.2 dBdsx (133-266 ft) +1.8 dBdsx (266-399 ft) +2.4 dBdsx (399-533 ft) +3.0 dBdsx (533-655 ft)					0 to 225 ft 225 to 450 ft	0 to 225 ft 255 to 450 ft	0 to 225 ft			
Cable simulation	Power dBdsx -22.5 dBdsx -15.0 dBdsx -7.5 dBdsx 0 dBdsx					450 to 900 (927) ft	450 to 900 (927) ft				
Rx level sensitivity	For 772 kHz: TERM: ≤ 26 dB (cable loss only) at 0 dBdsx Tx DSX-MON: ≤ 26 dB (20 dB resistive loss + cable loss ≤ 6 dB) Bridge: ≤ 6 dB (cable loss only) Note: measurement units = dBdsx	For 1024 kHz: TERM: ≤ 6 dB (cable loss only) MON: ≤ 25 dB (20 dB resistive loss + cable loss ≤ 6 dB) Bridge: ≤ 6 dB (cable loss only) Note: measurement units = dBm	For 1024 kHz: TERM: ≤ 6 dB (cable loss only) MON: ≤ 26 dB (20 dB resistive loss + cable loss ≤ 6 dB) Bridge: ≤ 6 dB (cable loss only) Note: measurement units = dBm	For 4224 kHz: TERM: ≤ 6 dB (cable loss only) MON: ≤ 26 dB (20 dB resistive loss + cable loss ≤ 6 dB) Note: measurement units = dBm	For 17184 MHz: TERM: ≤ 12 dB (coaxial cable loss only) MON: ≤ 26 dB (20 dB resistive loss + cable loss ≤ 6 dB) Note: measurement units = dBm	For 22368 MHz: TERM: ≤ 10 dB (cable loss only) DSX-MON: ≤ 26.5 dB (21.5 dB resistive loss + cable loss ≤ 5 dB) Note: measurement units = dBm	For 25.92 MHz: TERM: ≤ 10 dB (cable loss only) MON: ≤ 25 dB (20 dB resistive loss + cable loss ≤ 5 dB) Note: measurement units = dBm	For 70 MHz: TERM: ≤ 12 dB (coaxial cable loss only) MON: ≤ 26 dB (20 dB resistive loss + cable loss ≤ 6 dB) Note: measurement units = dBm	For 78 MHz: TERM: ≤ 12.7 dB (coaxial cable loss only) MON: ≤ 26 dB (20 dB resistive loss + cable loss ≤ 6 dB) Note: measurement units = dBm		
Tx bit rate	1.544 Mbit/s ± 4.6 ppm	2.048 Mbit/s ± 4.6 ppm	2.048 Mbit/s ± 4.6 ppm	8.448 Mbit/s ± 4.6 ppm	34.368 Mbit/s ± 4.6 ppm	44.736 Mbit/s ± 4.6 ppm	51.84 Mbit/s ± 4.6 ppm	139.264 Mbit/s ± 4.6 ppm	155.52 Mbit/s ± 4.6 ppm		
Rx bit rate	1.544 Mbit/s ± 140 ppm	2.048 Mbit/s ± 100 ppm	2.048 Mbit/s ± 100 ppm	8.448 Mbit/s ± 100 ppm	34.368 Mbit/s ± 100 ppm	44.736 Mbit/s ± 100 ppm	51.84 Mbit/s ± 100 ppm	139.264 Mbit/s ± 100 ppm	155.52 Mbit/s ± 100 ppm		
Measurement accuracy (uncertainty)											
Frequency	±4.6 ppm	±4.6 ppm	±4.6 ppm	±4.6 ppm	±4.6 ppm	±4.6 ppm	±4.6 ppm	±4.6 ppm	±4.6 ppm		
Electrical power	DSX range: ±1.0 dB DSX/MON range: ±2.0 dB	Normal: ±1.0 dB Monitor: ±2.0 dB	Normal: ±1.0 dB Monitor: ±2.0 dB	Normal: ±1.0 dB Monitor: ±2.0 dB	Normal: ±1.0 dB Monitor: ±2.0 dB	DSX range: ±1.0 dB DSX/MON range: ±2.0 dB	DSX range: ±1.0 dB DSX/MON range: ±2.0 dB	Normal: ±1.0 dB Monitor: ±2.0 dB	Normal: ±1.0 dB Monitor: ±2.0 dB		
Peak-to-peak voltage	±10% down to 500 mVpp	±10% down to 500 mVpp	±10% down to 500 mVpp	±10% down to 400 mVpp	±10% down to 200 mVpp	±10% down to 200 mVpp	±10% down to 200 mVpp	±10% down to 200 mVpp	±10% down to 200 mVpp		
Frequency offset generation	1.544 Mbit/s ± 140 ppm	2.048 Mbit/s ± 70 ppm	2.048 Mbit/s ± 70 ppm	8.448 Mbit/s ± 50 ppm	34.368 Mbit/s ± 50 ppm	44.736 Mbit/s ± 50 ppm	51.84 Mbit/s ± 50 ppm	139.264 Mbit/s ± 50 ppm	155.52 Mbit/s ± 50 ppm		
Intrinsic jitter (Tx)	ANSI T1.403 section 6.3 GR-499 section 7.3	G.823 section 5.1	G.823 section 5.1	G.823 section 5.1	G.823 section 5.1 G.751 section 2.3	GR-449 section 7.3 (categories I and II)	GR-253 section 5.6.2.2 (category II)	G.823 section 5.1	G.825 section 5.1 GR-253 section 5.6.2.2		
Input jitter tolerance	AT&T PUB 62411 GR-499 section 7.3	G.823 section 7.1	G.823 section 7.1	G.823 section 7.1	G.823 section 7.1	GR-449 section 7.3 (categories I and II)	GR-253 section 5.6.2.2 (category II)	G.823 section 7.1 G.751 section 3.3	G.825 section 5.2 GR-253 section 5.6.2.3		
Line coding	AMI and B8ZS	AMI and HDB3	AMI and HDB3	HDB3	HDB3	B3ZS	B3ZS	CMI	CMI		
Input impedance (resistive termination)	100 Ω ± 5%, balanced	120 Ω ± 5%, balanced	75 Ω ± 5%, unbalanced	75 Ω ± 5%, unbalanced	75 Ω ± 5%, unbalanced	75 Ω ± 5%, unbalanced	75 Ω ± 5%, unbalanced	75 Ω ± 10%, unbalanced	75 Ω ± 5%, unbalanced		
Connector type	BANTAM and RJ-48C	BANTAM and RJ-48C	BNC	BNC	BNC	BNC	BNC	BNC	BNC		

## SYNCHRONIZATION INTERFACES

	External Clock DS1/1.5M	External Clock E1/2M	External Clock E1/2M	2 MHz (Trigger)
Tx pulse amplitude	2.4 to 3.6 V	3.0 V	2.37 V	0.75 to 1.5 V
Tx pulse mask	GR-499 figure 9.5	G.703 figure 15	G.703 figure 15	G.703 figure 20
Tx LBO preamplification	Typical power dBdsx +0.6 dBdsx (0-133 ft) +1.2 dBdsx (133-266 ft) +1.8 dBdsx (266-399 ft) +2.4 dBdsx (399-533 ft) +3.0 dBdsx (533-655 ft)			
Rx level sensitivity	TERM: ≤ 6 dB (cable loss only) (at 772 kHz for T1) DSX-MON: ≤ 26 dB (20 dB resistive loss + cable loss ≤ 6 dB) Bridge: ≤ 6 dB (cable loss only)	TERM: = ≤ 6 dB (cable loss only) MON: ≤ 26 dB (20 dB resistive loss + cable loss ≤ 6 dB) Bridge: ≤ 6 dB (cable loss only)	TERM: = ≤ 6 dB (cable loss only) MON: ≤ 26 dB (resistive loss + cable loss ≤ 6 dB) Bridge: ≤ 6 dB (cable loss only)	≤ 6 dB (cable loss only)
Transmission bit rate	1.544 Mbit/s ± 4.6 ppm	2.048 Mbit/s ± 4.6 ppm	2.048 Mbit/s ± 4.6 ppm	
Reception bit rate	1.544 Mbit/s ± 50 ppm	2.048 Mbit/s ± 50 ppm	2.048 Mbit/s ± 50 ppm	
Intrinsic jitter (Tx)	ANSI T1.403 section 6.3 GR-499 section 7.3	G.823 section 6.1	G.823 section 6.1	G.703 table 11
Input jitter tolerance	AT&T PUB 62411 GR-499 SECTION 7.3	G.823 section 7.2 G.813	G.823 section 7.2 G.813	
Line coding	AMI and B8ZS	AMI and HDB3	AMI and HDB3	
Input impedance (resistive termination)	75 Ω ± 5%, unbalanced	75 Ω ± 5%, unbalanced	75 Ω ± 5%, unbalanced	75 Ω ± 5%, unbalanced
Connector type	BNC <sup>a</sup>	BNC <sup>a</sup>	BNC	BNC

**NOTE**

a. Adaptation cable required for BANTAM.

## Optical Interfaces

The following section provides detailed information on all supported optical interfaces.

	OC-3/STM-1				OC-12/STM-4				OC-48/STM-16			
	15 km; 1310 nm	40 km; 1310 nm	40 km; 1550 nm	80 km; 1550 nm	15 km; 1310 nm	40 km; 1310 nm	40 km; 1550 nm	80 km; 1550 nm	15 km; 1310 nm	40 km; 1310 nm	40 km; 1550 nm	80 km; 1550 nm
Level Tx	-5 to 0 dBm	-2 to 3 dBm	-5 to 0 dBm	-2 to 3 dBm	-5 to 0 dBm	-2 to 3 dBm	-5 to 0 dBm	-2 to 3 dBm	-5 to 0 dBm	-2 to 3 dBm	-5 to 0 dBm	-2 to 3 dBm
Rx level sensitivity	-18 to 0 dBm	-27 to -9 dBm	-18 to 0 dBm	-28 to -9 dBm	-18 to 0 dBm	-27 to -9 dBm	-18 to 0 dBm	-28 to -9 dBm	-18 to 0 dBm	-27 to -9 dBm	-18 to 0 dBm	-28 to -9 dBm
Transmit bit rate	155.52 Mbit/s ± 4.6 ppm				622.08 Mbit/s ± 4.6 ppm				2.48832 Gbit/s ± 4.6 ppm			
Receive bit rate	155.52 Mbit/s ± 100 ppm				622.08 Mbit/s ± 100 ppm				2.48832 Gbit/s ± 100 ppm			
Operational wavelength range	1260 to 1360 nm		1430 to 1580 nm		1274 to 1356 nm	1280 to 1335 nm	1430 to 1580 nm	1480 to 1580 nm	1260 to 1360 nm	1280 to 1335 nm	1430 to 1580 nm	1500 to 1580 nm
Spectral width	< 1 nm (-20 dB from center)				< 1 nm (-20 dB from center)				< 1 nm (-20 dB from center)			
Frequency offset generation	±50 ppm				±50 ppm				±50 ppm			
Measurement accuracy (uncertainty)												
Frequency	±4.6 ppm				±4.6 ppm				±4.6 ppm			
Optical power	±2 dB				±2 dB				±2 dB			
Maximum Rx before damage <sup>a</sup>	3 dB				3 dB				3 dB			
Jitter compliance	G.957 (SDH) GR-253 (SONET) G.958 (SDH)				GR-253 (SONET) G.958 (SDH)				GR-253 (SONET) G.958 (SDH)			
Line coding	NRZ				NRZ				NRZ			
Eye safety	SFP/XFP transceivers comply with IEC 60825 and 21 CFR 1040.10 (except for deviations pursuant to Laser Notice No. 50, dated July 2001), for Class 1 or 1M lasers.											
Connector <sup>b</sup>	Dual LC				Dual LC				Dual LC			
Transceiver type	SFP				SFP				SFP			

### NOTES

- a. In order not to exceed the maximum receiver power level before damage, an attenuator must be used.
- b. SFP compliance: The IQS-8115 selected SFP shall meet the requirements stated in the "Small Form-Factor Pluggable (SFP) Transceiver Multisource Agreement (MSA)".  
 The IQS-8115 selected SFP shall meet the requirements stated in the "Specification for Diagnostic Monitoring Interface for Optical Xcvrs".

# Functional Specifications

SONET AND DS <sub>n</sub>		SDH AND PDH	
<b>Optical interfaces</b>	OC-3, OC-12, OC-48	<b>Optical interfaces</b>	STM-1, STM-4, STM-16
Available wavelengths (nm)	1310, 1550	Available wavelengths (nm)	1310, 1550
<b>Electrical interfaces</b>	DS1, DS3, STS-1e, STS-3e	<b>Electrical interfaces <sup>a</sup></b>	1.5M (DS1), 2M (E1), 8M (E2), 34M (E3), 45M (DS3), 140M (E4), STM-0e, STM-1e
DS1 framing	Unframed, SF, ESF	2M framing	Unframed, PCM30, PCM31, PCM30 CRC-4, PCM31 CRC-4
DS3 framing	Unframed, M13, C-bit parity	8M, 34M, 140M framing	Unframed, framed
Clocking	Internal, loop-timed, external (BITS), inter-module	Clocking	Internal, loop-timed, external (MTS/SETS), 2 MHz, inter-module
<b>Mappings</b>		<b>Mappings</b>	
VT1.5	Bulk, DS1	TU-11-AU-3, TU-11-AU-4	Bulk, 1.5M
VT2	Bulk, E1	TU-12-AU-3, TU-12-AU-4	Bulk, 2M
VT6	Bulk	TU-3-AU-4	Bulk, 34M, 45M
STS-1	Bulk, DS3	TU-2-AU-3, TU-2-AU-4	Bulk
STS-3c	Bulk, E4	AU-4	Bulk, 140M
STS-12c/48c	Bulk	AU-4-4c/16c	Bulk
SONET overhead analysis and manipulation	A1, A2, J0, E1, F1, D1-D12, K1, K2, S1, M0, E2, J1, C2, G1, F2, H4, Z3, Z4, Z5, N1, N2, Z6, Z7	SDH overhead analysis and manipulation	A1, A2, J0, E1, F1, D1-D12, K1, K2, S1, M0, E2, J1, C2, G1, F2, F3, K3, N1, N2, K4, H4
<b>Error insertion</b>		<b>Error insertion</b>	
DS1	Framing bit, BPV, CRC-6, bit error	E1 (2M)	FAS, CV, CRC-4, E-bit, bit error
DS3	BPV, C-bit, F-bit, P-bit, FEBE, bit error	E2 (8M), E3 (34M), E4 (140M)	FAS, CV, bit error
STS-1e, STS-3e	Section BIP (B1), line BIP (B2), path BIP (B3), BIP-2, REI-L, REI-P, REI-V, BPV, FAS, bit error	STM-0e, STM-1e	RS-BIP (B1), MS-BIP (B2), HP-BIP (B3), MS-REI, HP-REI, LP-BIP-2, LP-REI, CV, FAS, bit error
OC-3, OC-12, OC-48	Section BIP (B1), line BIP (B2), path BIP (B3), BIP-2, REI-L, REI-P, REI-V, FAS, bit error	STM-1, STM-4, STM-16	RS-BIP (B1), MS-BIP (B2), HP-BIP (B3), MS-REI, HP-REI, LP-BIP-2, LP-REI, FAS, bit error
<b>Error measurement</b>		<b>Error measurement</b>	
DS1	Framing bit, BPV, CRC-6, excess zeros, bit error	E1 (2M)	FAS, CV, CRC-4, E-bit, bit error
DS3	BPV, C-bit, F-bit, P-bit, FEBE, bit error	E2 (8M), E3 (34M), E4 (140M)	FAS, CV, bit error
STS-1e, STS-3e	Section BIP (B1), line BIP (B2), path BIP (B3), BIP-2, REI-L, REI-P, REI-V, BPV, FAS, bit error	STM-0e, STM-1e	RS-BIP (B1), MS-BIP (B2), HP-BIP (B3), MS-REI, HP-REI, LP-BIP-2, LP-REI, CV, FAS, bit error
OC-3, OC-12, OC-48	Section BIP (B1), line BIP (B2), path BIP (B3), BIP-2, REI-L, REI-P, REI-V, FAS, bit error	STM-1, STM-4, STM-16	RS-BIP (B1), MS-BIP (B2), HP-BIP (B3), MS-REI, HP-REI, LP-BIP-2, LP-REI, FAS, bit error
<b>Alarm insertion</b>		<b>Alarm insertion</b>	
DS1	LOS, RAI, AIS, OOF, pattern loss	E1 (2M)	LOS, LOS Mframe, LOS CRC Mframe, LOF, AIS, TS16 AIS, RAI, RAI Mframe, pattern loss
DS3	LOS, RDI, AIS, OOF, DS3 idle, pattern loss	E2 (8M), E3 (34M), E4 (140M)	LOS, LOF, RAI, AIS, pattern loss
STS-1e, STS-3e, OC-3, OC-12, OC-48	LOS, LOF, SEF, AIS-L, RDI-L, AIS-P, LOP-P, LOM, PDI-P, RDI-P, ERDI-PCD, ERDI-PPD, ERDI-PSD, UNEQ-P, AIS-V, LOP-V, RDI-V, ERDI-VCD, ERDI-VPD, ERDI-VSD, RFI-V, UNEQ-V, pattern loss	STM-0e, STM-1e, STM-1, STM-4, STM-16	LOS, LOF, OOF, MS-AIS, MS-RDI, AU-AIS, AU-LOP, H4-LOM, HP-PDI, ERDI-PSD, ERDI-PCD, ERDI-PPD, HP-UNEQ, TU-AIS, LP-RFI, LP-RDI, ERDI-VCD, ERDI-VPD, ERDI-VSD, LP-RFI, LP-UNEQ, pattern loss
<b>Alarm detection</b>		<b>Alarm detection</b>	
DS1	LOS, loss of clock (LOC), RAI, AIS, OOF, pattern loss	E1 (2M)	LOS, LOS Mframe, LOS CRC Mframe, LOC, LOF, AIS, TS16 AIS, RAI, RAI Mframe, pattern loss
DS3	LOS, LOC, RDI, AIS, OOF, DS3 idle, pattern loss	E2 (8M), E3 (34M), E4 (140M)	LOS, LOC, LOF, RAI, AIS, pattern loss
STS-1e, STS-3e, OC-3, OC-12, OC-48	LOS, LOC, LOF, SEF, TIM-S, AIS-L, RDI-L, AIS-P, LOP-P, LOM, PDI-P, RDI-P, ERDI-PCD, ERDI-PPD, ERDI-PSD, PLM/SLM-P, UNEQ-P, TIM-P, AIS-V, LOP-V, RDI-V, ERDI-VCD, ERDI-VPD, ERDI-VSD, RFI-V, UNEQ-V, TIM-V, PLM/SLM-V, pattern loss	STM-0e, STM-1e, STM-1, STM-4, STM-16	LOS, LOF, LOC, OOF, RS-TIM, MS-AIS, MS-RDI, AU-AIS, AU-LOP, H4-LOM, HP-RDI, ERDI-PSD, ERDI-PCD, ERDI-PPD, HP-PLM/SLM, HP-UNEQ, HP-TIM, TU-AIS, LP-RFI, LP-RDI, ERDI-VCD, ERDI-VPD, ERDI-VSD, LP-RFI, LP-UNEQ, LP-TIM, LP-PLM/SLM, pattern loss
<i>Frequency alarm on all supported interfaces.</i>			
<b>Patterns</b>		<b>Patterns</b>	
DS0	2E9-1, 2E11-1, 2E20-1, 1100, 1010, 1111, 0000, 1-in-8, 1-in-16, 3-in-24, 32 bit programmable (inverted or non-inverted), bit errors	E0 (64K)	2E9-1, 2E11-1, 2E20-1, 1100, 1010, 1111, 0000, 1-in-8, 1-in-16, 3-in-24, 32 bit programmable (inverted or non-inverted), bit errors
DS1	2E9-1, 2E11-1, 2E15-1, 2E20-1, 2E23-1, 2E31-1, 1100, 1010, 1111, 0000, QRSS, 1-in-8, 1-in-16, 3-in-24, 32 bit programmable (inverted or non-inverted), T1-DALY, 55-Octet, bit errors	E1 (2M)	2E9-1, 2E11-1, 2E15-1, 2E20-1, 2E23-1, 2E31-1, 1100, 1010, 1111, 0000, 1-in-8, 1-in-16, 3-in-24, 32 bit programmable (inverted or non-inverted), bit errors
DS3	2E9-1, 2E11-1, 2E15-1, 2E20-1, 2E23-1, 2E31-1, 1100, 1010, 1111, 0000, QRSS, 1-in-8, 1-in-16, 3-in-24, 32 bit programmable (inverted or non-inverted), bit errors	E2 (8M), E3 (34M), E4 (140M)	2E9-1, 2E11-1, 2E15-1, 2E20-1, 2E23-1, 2E31-1, 1100, 1010, 1111, 0000, 1-in-8, 1-in-16, 3-in-24 <sup>b</sup> , 32 bit programmable (inverted or non-inverted), bit errors
VT1.5/2/6	2E9-1, 2E11-1, 2E15-1, 2E20-1, 2E23-1, 2E31-1, 1100, 1010, 1111, 0000, QRSS, 1-in-8, 1-in-16, 32 bit programmable (inverted or non-inverted), bit errors	TU-11/12/2/3	2E9-1, 2E11-1, 2E15-1, 2E20-1, 2E23-1, 2E31-1, 1100, 1010, 1111, 0000, 1-in-8, 1-in-16, 32 bit programmable (inverted or non-inverted), bit errors
STS-1/STS-3c/12c/48c	2E9-1, 2E11-1, 2E15-1, 2E20-1, 2E23-1, 2E31-1, 1100, 1010, 1111, 0000, 1-in-8, 1-in-16, 32 bit programmable (inverted or non-inverted), bit errors	AU-3/AU-4/AU-4-4c/16c	2E9-1, 2E11-1, 2E15-1, 2E20-1, 2E23-1, 2E31-1, 1100, 1010, 1111, 0000, 1-in-8, 1-in-16, 32 bit programmable (inverted or non-inverted), bit errors

*Pattern loss and bit error generation and analysis supported on all patterns.*

## NOTES

- 1.5M (DS1) and 45M (DS3) interfaces described under SONET and DS<sub>n</sub> column.
- Not supported for E4 (140M).

### ADDITIONAL TEST AND MEASUREMENT FUNCTIONS

Power measurements	Supports power measurements, displayed in dBm (dBdsx for DS1), for optical and electrical interfaces.
Frequency measurements	Supports clock frequency measurements (i.e., received frequency and deviation of the input signal clock from nominal frequency), displayed in ppm and b/s (bps), for optical and electrical interfaces.
Frequency offset generation	Supports offsetting the clock of the transmitted signal on a selected interface to exercise clock recovery circuitry on network elements.
Dual DSn receivers	Supports two DS1 or DS3 receivers, allowing users to simultaneously monitor two directions of a circuit under test in parallel, resulting in quick isolation of the source of errors.
Performance monitoring	The following ITU-T recommendations, and corresponding performance monitoring parameters, are supported on the IQS-8115.
ITU-T recommendation	Performance monitoring statistics
G.821	ES, EFS, EC, SES, UAS, ESR, SESR, DM
G.826	ES, EFS, EB, SES, BBE, UAS, ERS, SESR, BBER
G.828	ES, EFS, EB, SES, BBE, SEP, UAS, ESR, SESR, BBER, SEPI
G.829	ES, EFS, EB, SES, BBE, UAS, ESR, SESR, BBER
M.2100	ES, SES, UAS, ESR, SESR
M.2101	ES, SES, BBE, UAS, ESR, SESR, BBER
Pointer adjustment and analysis	Generation and analysis of HO/AU and LO/TU pointer adjustments as per GR-253, and ITU-T G.703
Generation	Analysis
<ul style="list-style-type: none"> <li>• Pointer increment and decrement</li> <li>• Pointer jump with or without NDF</li> <li>• Pointer value</li> </ul>	<ul style="list-style-type: none"> <li>• Pointer increments</li> <li>• Pointer decrements</li> <li>• Pointer jumps (NDF, no NDF)</li> <li>• Pointer value and cumulative offset</li> </ul>
Programmable error/alarm injection	Ability to inject errors/alarms in the following modes: Manual, Constant Rate, Burst, Periodic Burst and Continuous.
Service disruption time (SDT) measurements	The service disruption time test tool measures the time during which there is a disruption of service due to the network switching from the active channels to the backup channels. User-selectable triggers: All supported alarms and errors. Measurements: last disruption, shortest disruption, longest disruption, average disruption, total disruption, and service disruption count.
Round-trip delay (RTD) measurements	The round-trip delay test tool measures the time required for a bit to travel from the IQS-8115 transmitter back to its receiver after crossing a far-end loopback. Measurements are supported on all supported IQS-8115 interfaces and mappings. Measurements: last RTD time, minimum, maximum, average, measurement count (no. of successful RTD tests), failed measurement count.
APS message control and monitoring	Ability to monitor and set up automatic protection switching messages (K1/K2 byte of SONET/SDH overhead).
Synchronization status	Ability to monitor and set up synchronization status messages (S1 byte of SONET/SDH overhead).
Signal label control and monitoring	Ability to monitor and set up payload signal labels (C2, V5 byte of SONET/SDH overhead).
Through mode	Ability to perform Through mode analysis of any incoming electrical (DSn, PDH) and optical line (OC-3/12/48, STM-1/4/16).
M13 mux/demux	Ability to multiplex/demultiplex a DS1 signal into/from a DS3 signal. (Note: E1 to DS3 mux/demux available with G.747 software option.)
DS1 FDL	Support for DS1 Facility Data Link testing.
DS1 loopcodes	Support for generation of DS1 in-band loopcodes with the availability of up to 10 pairs of user-defined loopcodes.
NI/CSU loopback emulation	Ability to respond to DS1 in-band/out-of-band loopcodes.
DS3 FEAC	Support for DS3 far-end alarms and loopback codewords.
DS1/DS3 auto detection	Ability to automatically detect DS1/DS3 line coding, framing and test pattern.
Tandem connection monitoring (TCM) <sup>a</sup>	Tandem connection monitoring (TCM), option 2 <sup>b</sup> , is used to monitor the performance of a subsection of a SONET/SDH path routed via different network providers. The IQS-8115 supports transmitting and receiving alarms and errors on a TCM link; also, transmission and monitoring of the tandem connection (TC) trace can be generated to verify the connection between TCM equipment. Error generation: TC-IEC, TC-BIP, TC-REI, OEI Error analysis: TC-IEC, TC-REI, OEI, TC-VIOL Alarm generation: TC-RDI, TC-UNEQ, ODI, TC-LTC, TC-IAIS Alarm analysis: TC-TIM, TC-RDI, TC-UNEQ, ODI, TC-LTC, TC-IAIS

### ADDITIONAL FEATURES

Scripting	Wide range of SCPI commands powerful enough to provide repeatable testing of complex configuration, yet simple enough to create a 2.5 gigabit BERT in as little as seven commands. The IQS-8115 also includes an intuitive macro recorder enabling users to easily record test actions and automatically create test scripts in VB.Net.
Reports	Supports generation of test reports in .html, .csv, .txt, .pdf formats. Contents of reports are customizable by the user.
Power-up and restore	In the event of a power failure to the unit, the active test configuration and test logger are saved and restored upon bootup.
Store and load configurations	Ability to store and load test configurations to/from non-volatile memory.
Alarm hierarchy	Alarms are displayed according to a hierarchy based on root cause. Secondary effects are not displayed. This hierarchy serves to facilitate alarm analysis.
Configurable test views	This allows users to customize their test views, i.e., to dynamically insert or remove test tabs/windows, in addition to creating new test windows, so as to accurately match their testing needs.
Configurable test timer	Provides the ability for a user to set pre-defined test start and stop times.
Remote control	Available with Windows-based remote management software known as Visual Guardian Lite (optional software package). This allows users to remotely monitor and control the IQS-8115 module via standard Ethernet connection.

#### NOTES

- a. HOP and LOP supported.      b. G.707 option 2.

### SPECIFICATIONS

#### IQS-8115

SONET/SDH 155 Mbit/s, 622 Mbit/s and 2.5 Gbit/s  
 Analyzer module supporting up to OC-48/STM-16 optical rates, as well as electrical DSn/PDH interfaces

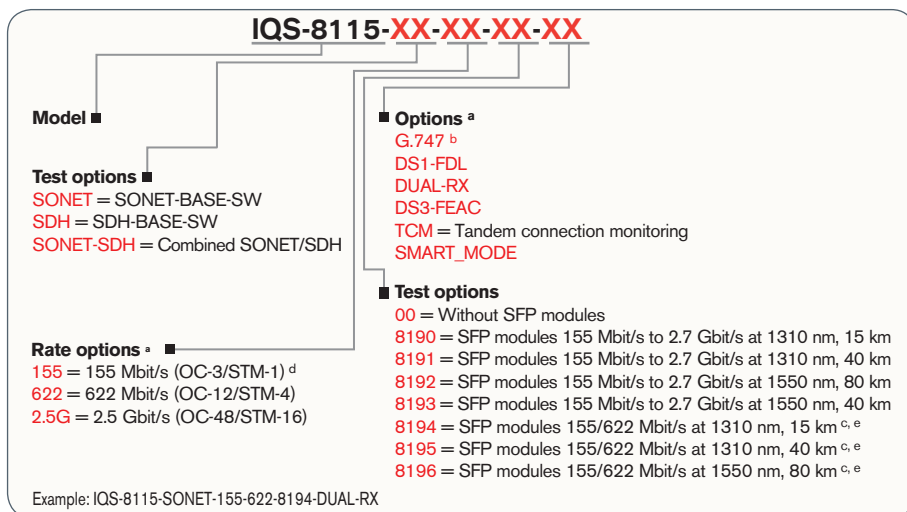
#### Test Interfaces

SONET: STS-1e, STS-3e, OC-3, OC-12, OC-48  
 SDH: STM-0e, STM-1e, STM-1, STM-4, STM-16  
 DSn: DS1, DS3, Dual DS1 Rx, Dual DS3 Rx  
 PDH: E1, E2, E3, E4

### GENERAL SPECIFICATIONS

	IQS-8115
Weight (without transceiver)	0.9 kg (2.0 lb)
Size (H x W x D)	125 mm x 76 mm x 282 mm (4 15/16 in x 3 in x 11 1/8 in)
Temperature	
operating	0 °C to 40 °C (32 °F to 104 °F)
storage	-40 °C to 60 °C (-40 °F to 140 °F)

## ORDERING INFORMATION



### NOTES

- Enables E1/2M in DS3/45M analysis and generation, as per ITU-T G.747 recommendation.
- Multiple options can be purchased to suit the required test application.
- For 8194, 8195, 8196 optical interface specifications, please contact your local EXFO representative.
- Always included.
- Not available with 2.5 Gbit/s.

## Complementary Products

### FTB-8080 Sync Analyzer

The FTB-8080 Sync Analyzer is a comprehensive test solution for telecom network synchronization assurance, monitoring and troubleshooting applications. It offers a full range of wander and sync testing functionalities, including graphical display of TIE, MTIE and TDEV parameters, as well as comparison to ITU/ANSI/TS standards and user-definable masks. The companion Sync View software suite allows remote data retrieval and test case setup, eliminating the need to visit test sites during prolonged monitoring periods. The FTB-8080 can be used in conjunction with the IQS-8105, IQS-8115 and/or IQS-8120/8130 modules to provide wander measurements up to OC-192/STM-64 rates.

For more information on the FTB-8080, please refer to its detailed product specification sheet at <http://documents.EXFO.com/specsheets/FTB-8080-ang.pdf>

### IQS-8120NGE/8130NGE Power Blazer Next-Generation Multiservice Test Modules

These modules offer a full suite of SONET/SDH, next-gen SONET/SDH and Ethernet testing capabilities inside the industry's most compact form factor, meeting all multiservice transport network testing needs. The IQS-8130NGE, supporting testing rates up to 10 Gbit/s for SONET/SDH/OTU2, Ethernet (including 10 Gigabit Ethernet LAN/WAN/OTU2e) and Fibre Channel (1x, 2x, 4x and 10x), is ideally suited for MSTP and ROADM testing and troubleshooting activities. The IQS-8120NGE, handling SONET/SDH testing rates up to 2.5 Gbit/s and Ethernet testing rates up to GigE, is purpose-built for the growing deployments of Ethernet-over-TDM and Ethernet-over-SONET/SDH services worldwide.

For details on the IQS-8120NGE/8130NGE modules, please refer to the detailed product specification sheet at <http://documents.EXFO.com/specsheets/IQS-8120-8130-ang.pdf>

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EXFO is certified ISO 9001 and attests to the quality of these products. This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation. EXFO has made every effort to ensure that the information contained in this specification sheet is accurate. However, we accept no responsibility for any errors or omissions, and we reserve the right to modify design, characteristics and products at any time without obligation. Units of measurement in this document conform to SI standards and practices. In addition, all of EXFO's manufactured products are compliant with the European Union's WEEE directive. For more information, please visit [www.EXFO.com/recycle](http://www.EXFO.com/recycle). Contact EXFO for prices and availability or to obtain the phone number of your local EXFO distributor.

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